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FIG. 1

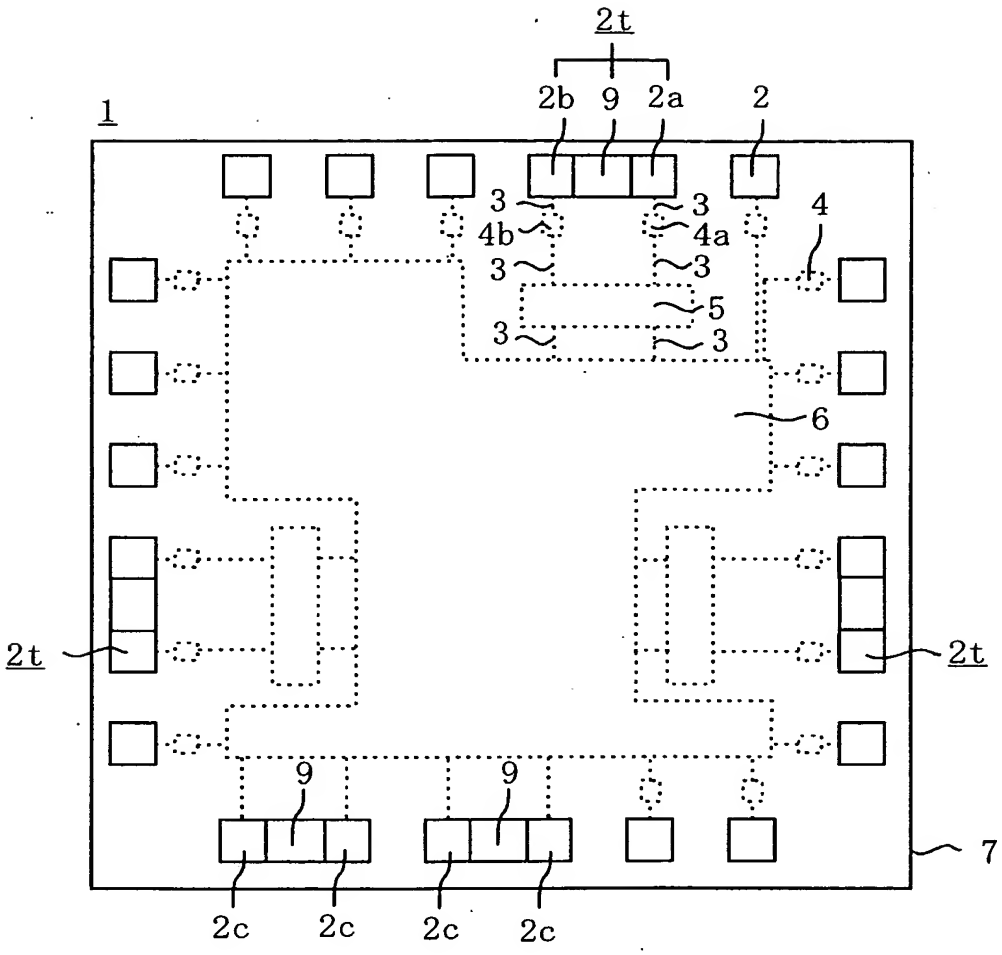


FIG. 2A

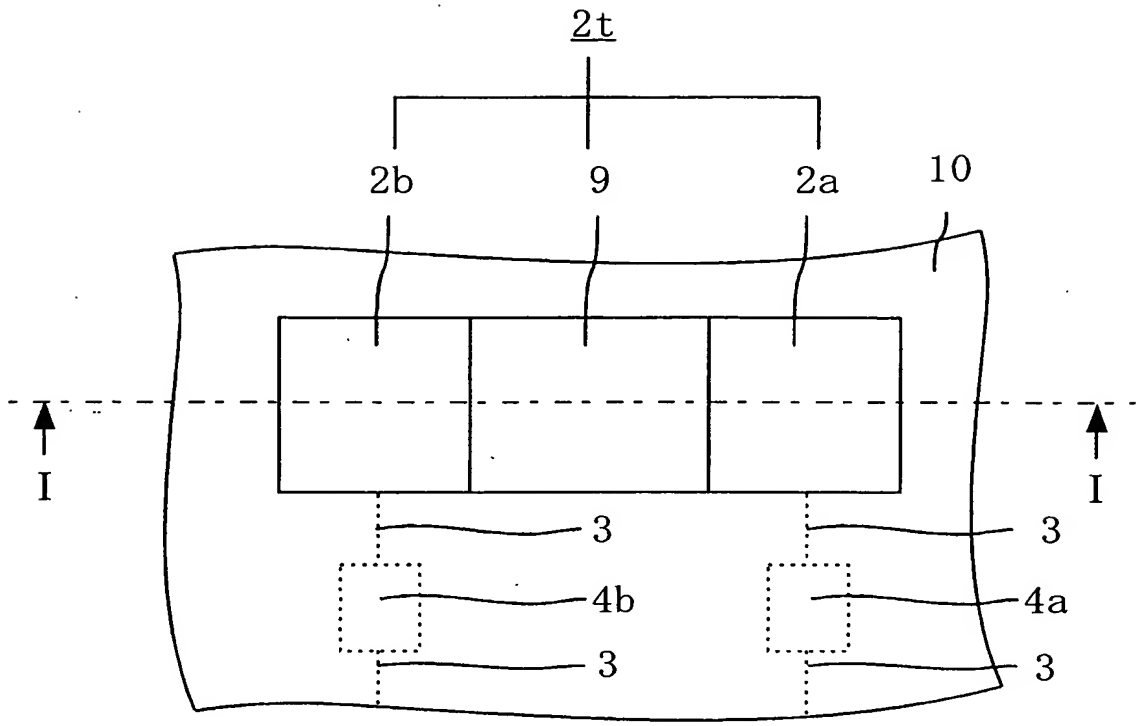


FIG. 2B

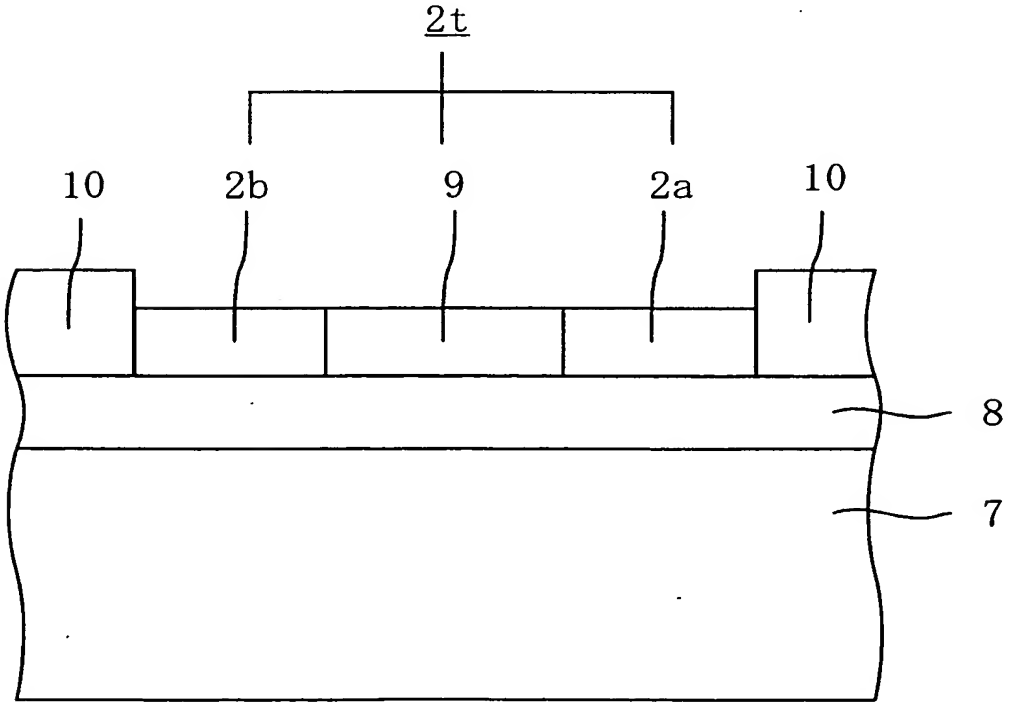


FIG. 3

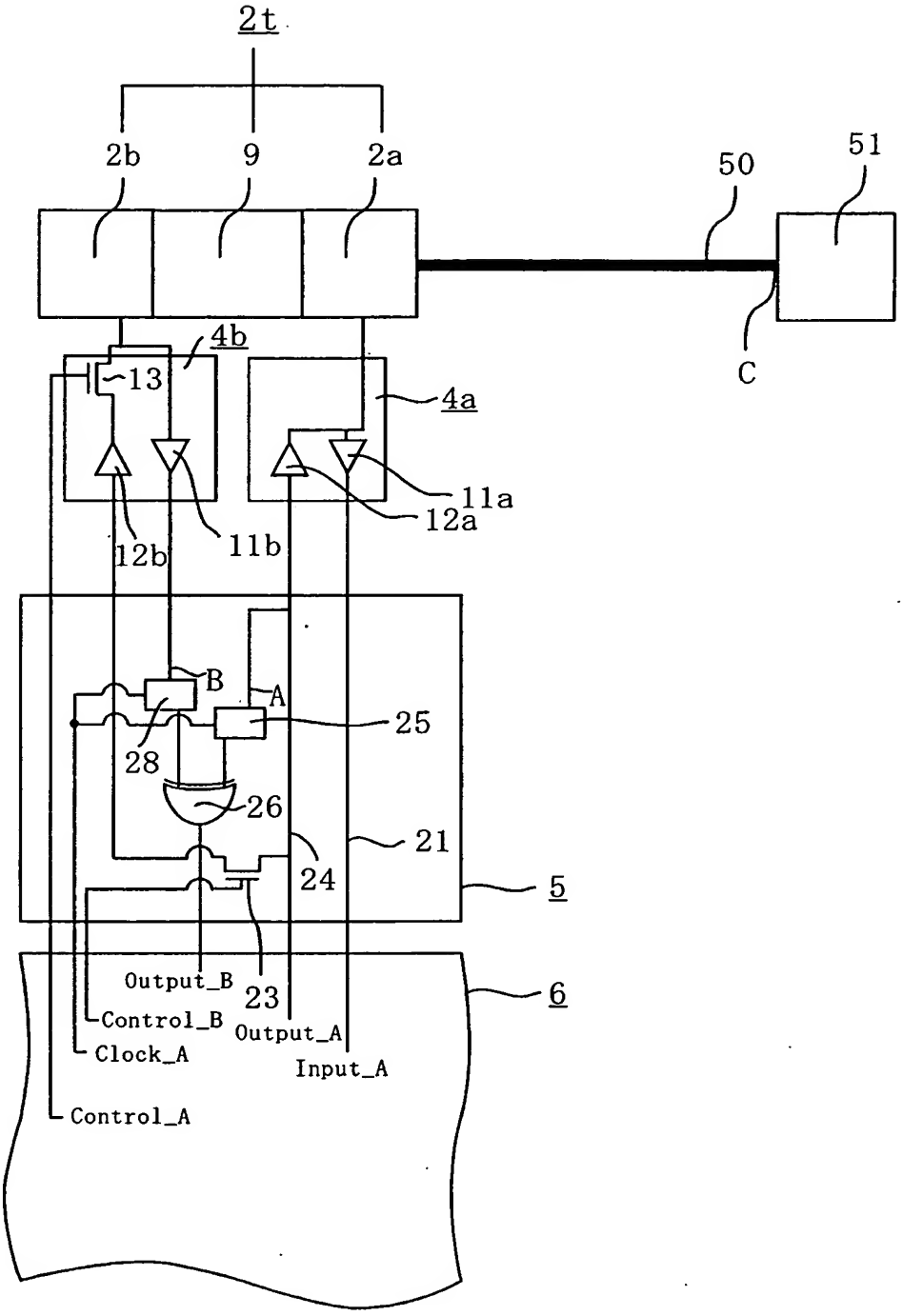

2t
2b
9
2a
50
51
C
4b
13
4a
11a
12a
12b
11b
B
A
25
28
26
21
24
5
6
Output_B
23
Control_B
Output_A
Clock_A
Input_A
Control_A

FIG. 4

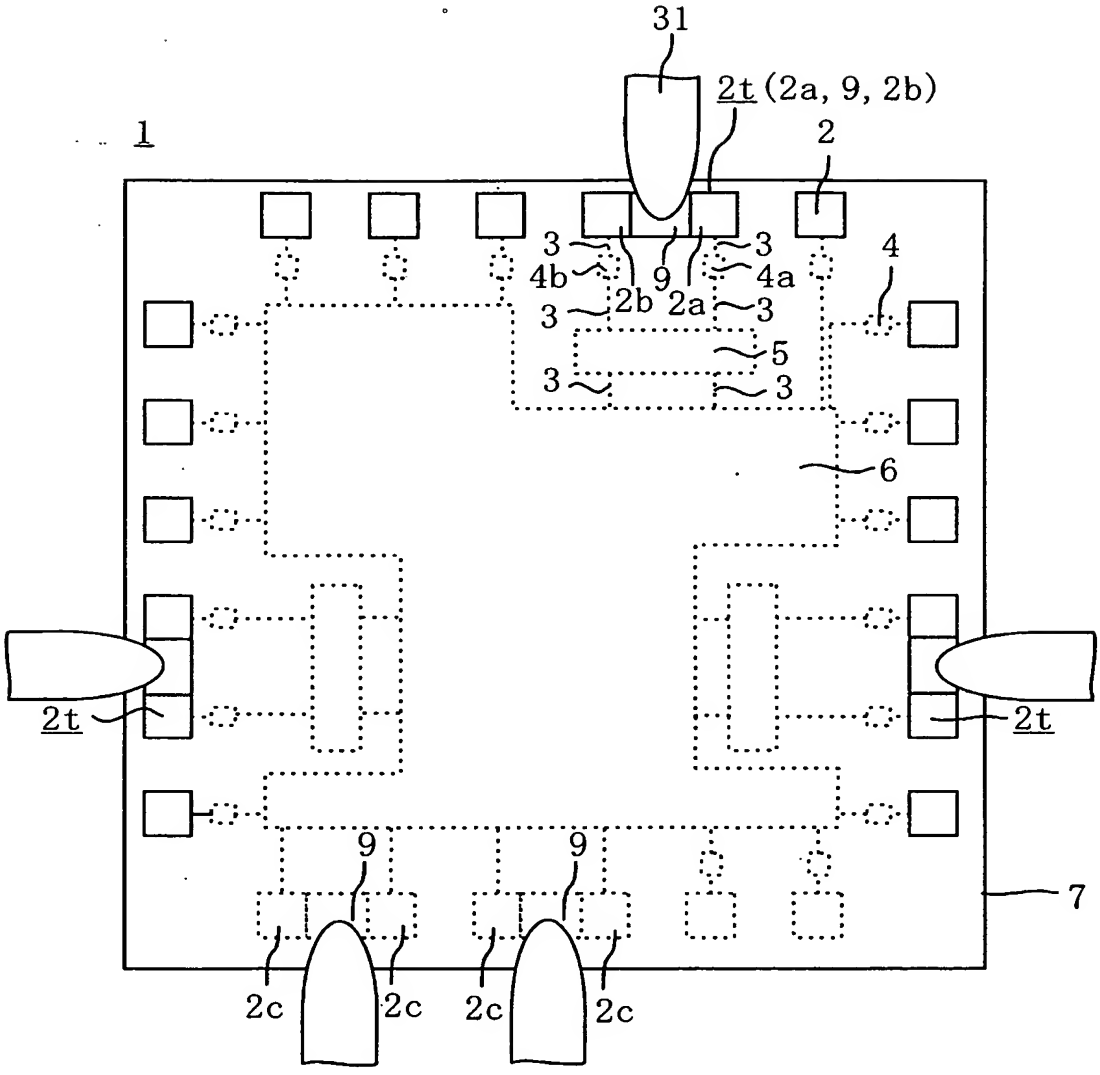


FIG. 5

Operation Mode	Signal pin name / Test pad input/output status	Control_A	Control_B	Clock_A
Normal operation mode	Input	0	0	0
	Output	0	0	0
First test mode	Input	0	0	0
	Output	1	1	0
Second test mode	Input	0	0	0
	Output	0	0	1

FIG. 6A

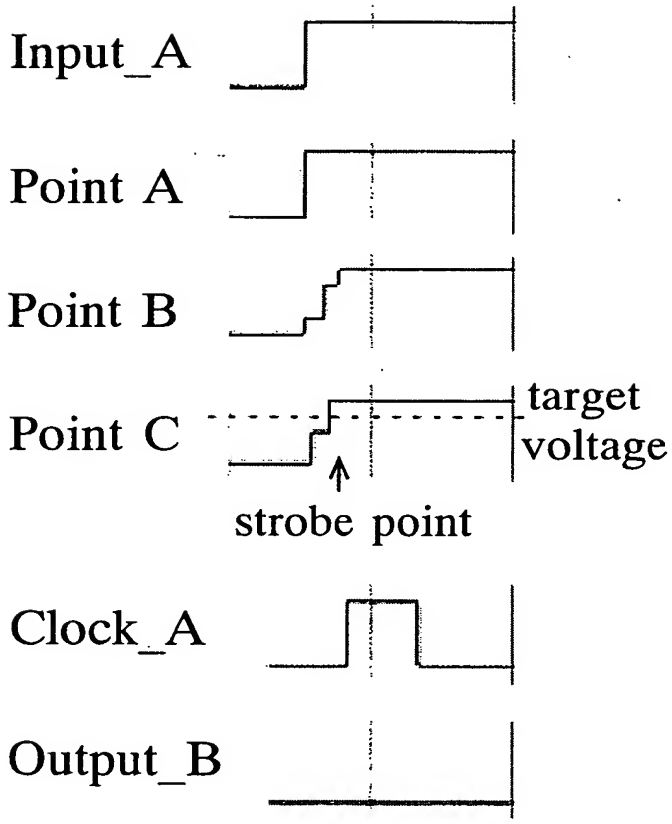

Input_A
Point A
Point B
Point C
target voltage
strobe point
Clock_A
Output_B

FIG. 6B

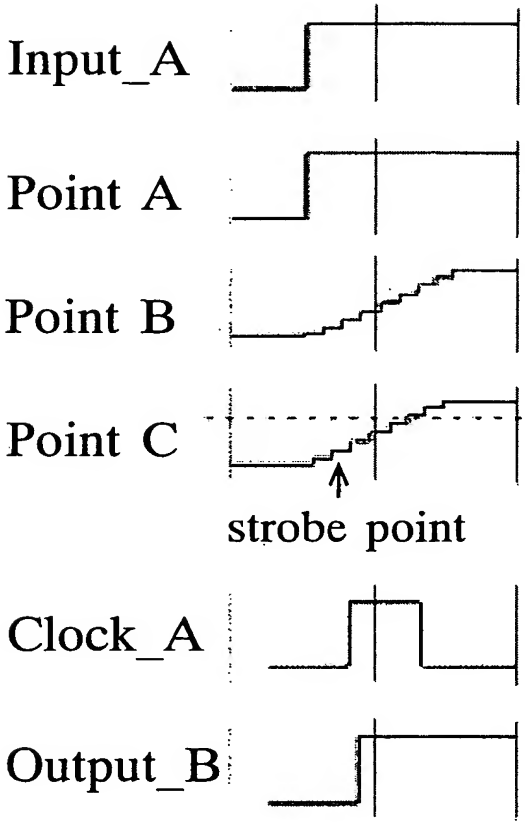

Input_A
Point A
Point B
Point C
strobe point
Clock_A
Output_B

FIG. 7

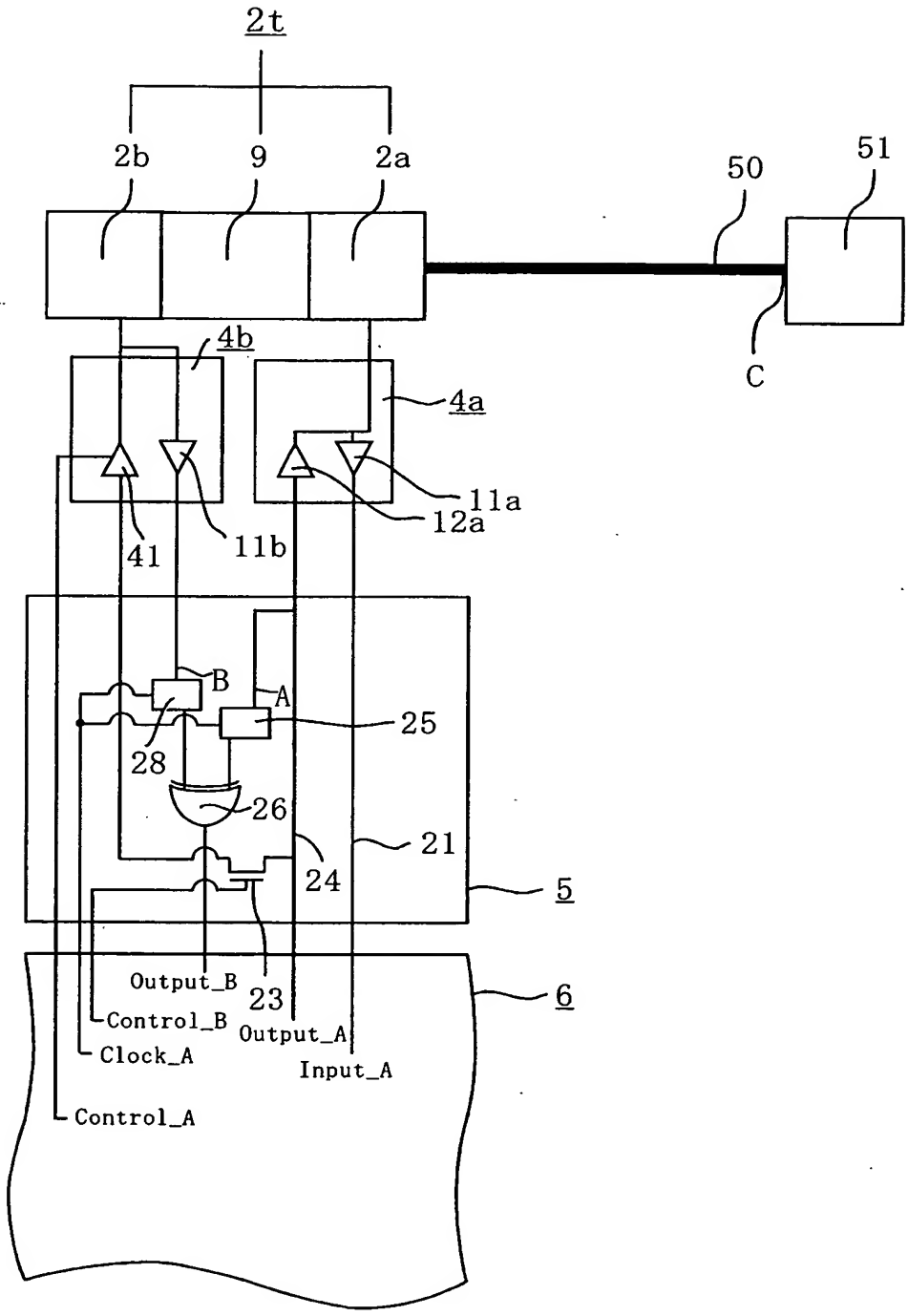

2t
2b
9
2a
50
51
C
4b
4a
11a
12a
41
11b
B
A
25
28
26
21
24
5
6
Output_B
23
Control_B
Output_A
Clock_A
Input_A
Control_A

FIG. 8A

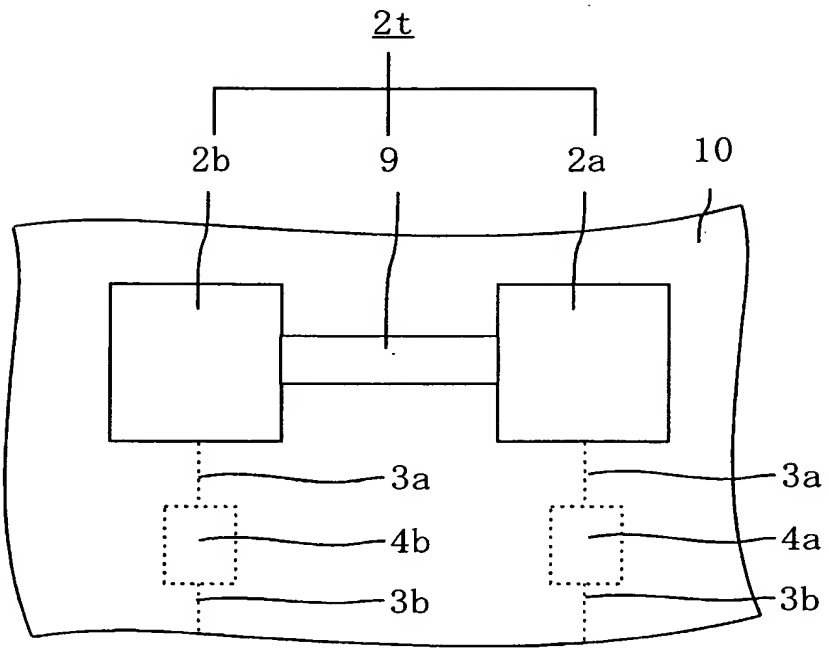


FIG. 8B

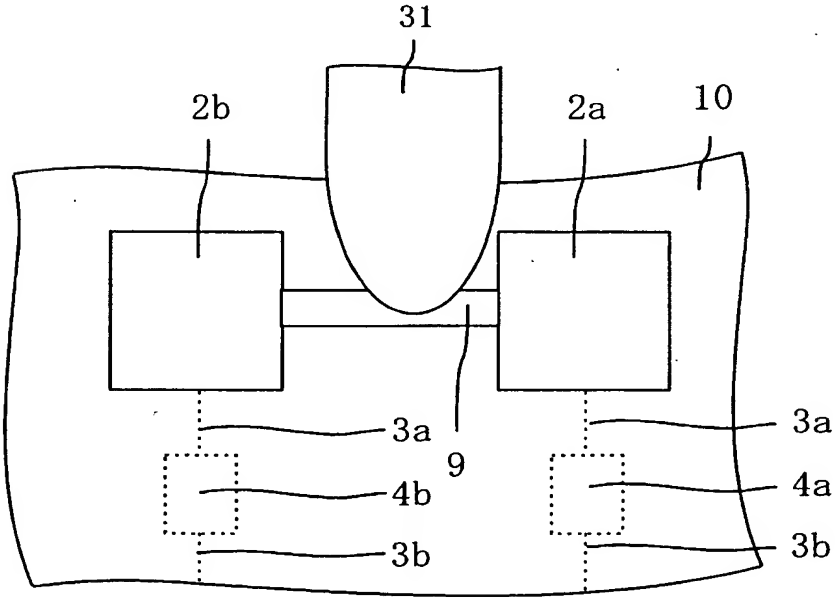


FIG. 9

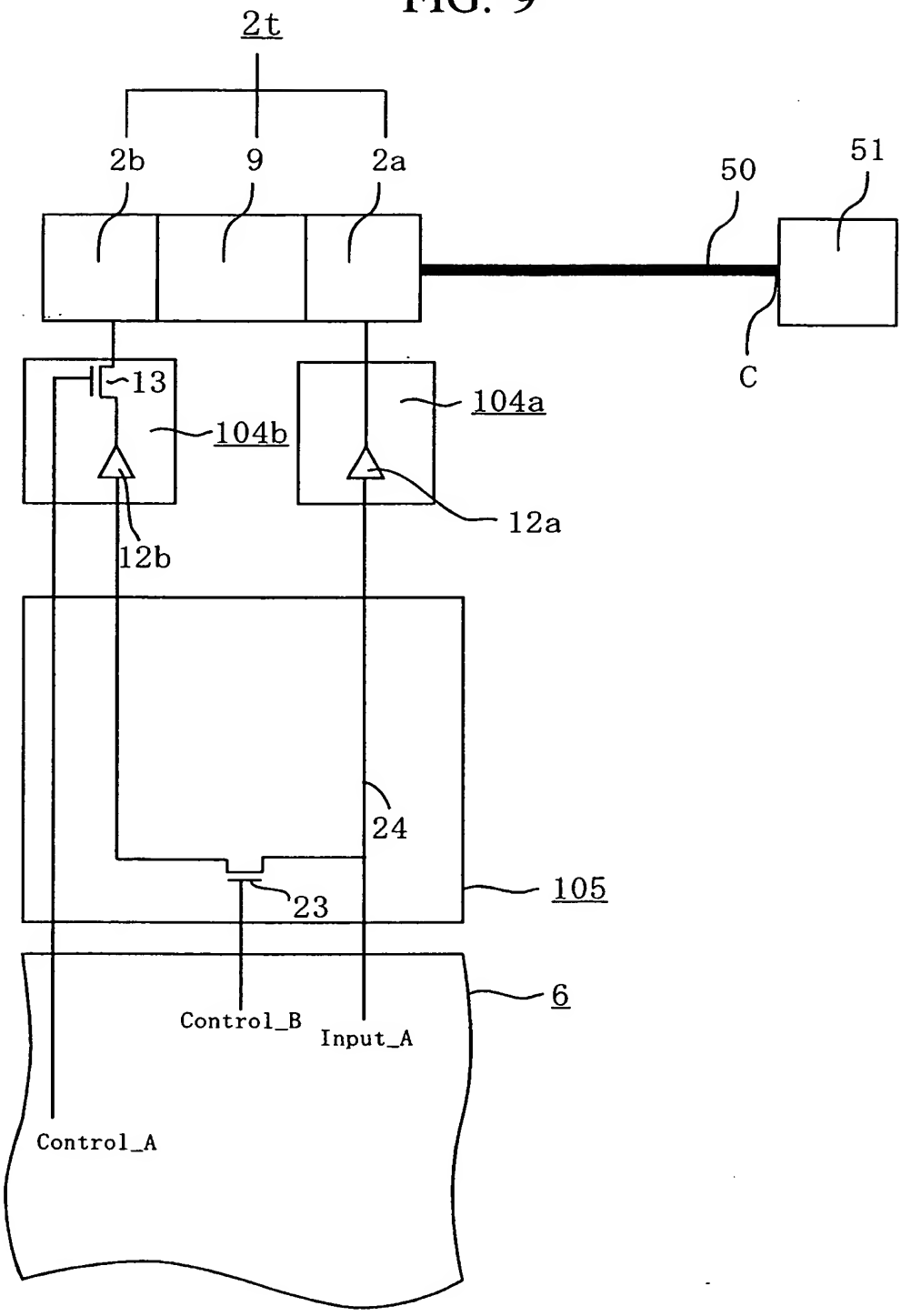
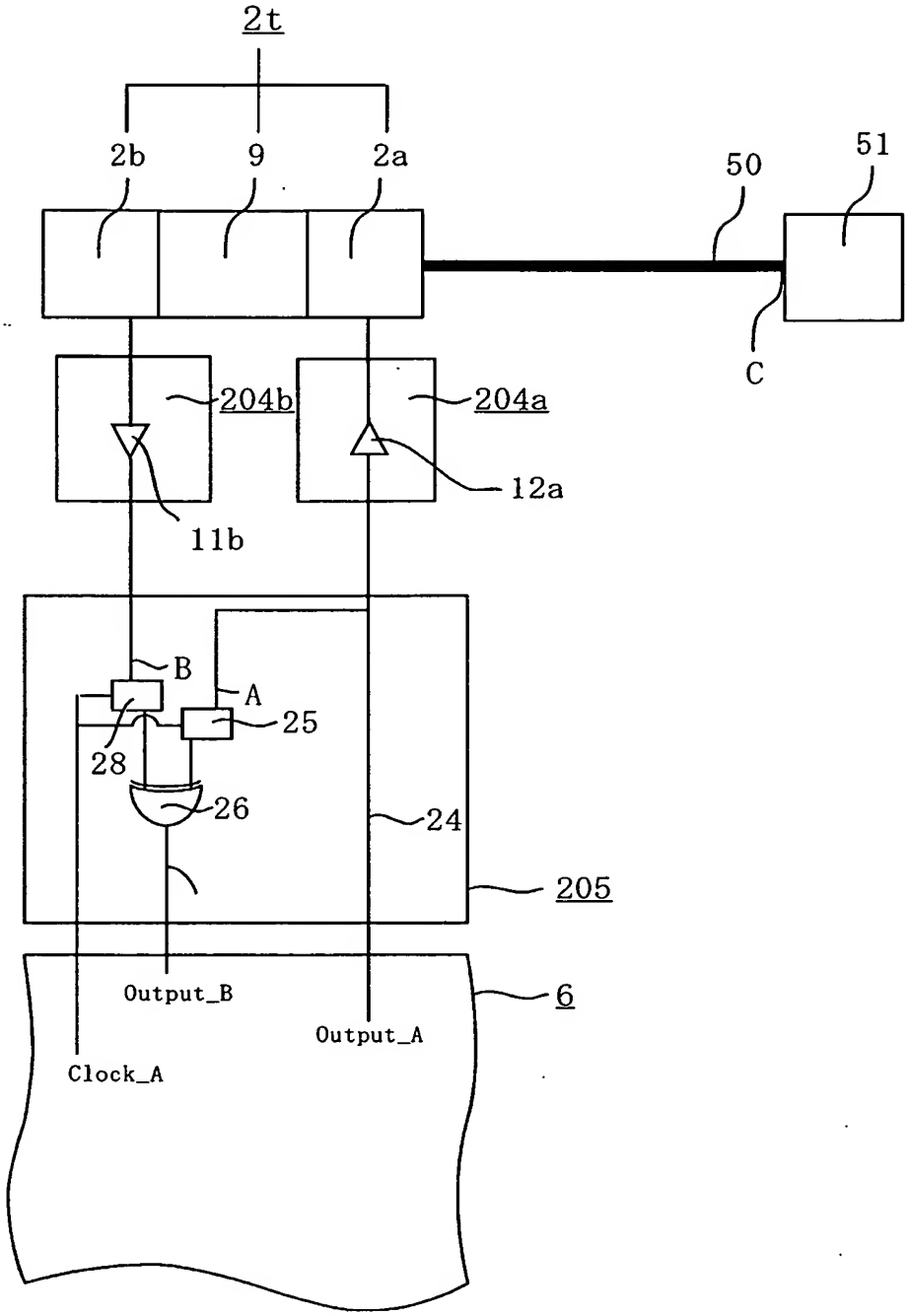

2t
2b
9
2a
50
51
C
13
104b
104a
12b
12a
24
23
105
6
Control_B
Input_A
Control_A

FIG. 10


2t
2b
9
2a
50
51
C
204b
204a
12a
11b
B
A
25
28
26
24
205
6
Output_B
Output_A
Clock_A